

IN THE SPECIFICATION:

Please amend the specification as follows:

[0003] Figure 1 is a circuit diagram of a prior art level shifter. Level shifter 101 includes circuitry located in a voltage domain 1 (Domain 1) of an integrated circuit and circuitry located in a voltage domain 2 (Domain 2) of an integrated circuit. A voltage domain is a portion of an integrated circuit that is operable at a given supply voltage. An inverter [[109]] 119 is located in Domain 1. N type field effect transistors (NFETs) 117 and 115 and P-type field effect transistors (PFETs) 111 and 113 are located in Domain 2. Inverter 119 includes a power supply terminal coupled to a Domain 1 voltage supply rail 109 (at voltage VDD1). A source/drain terminal (a current terminal of a FET) of PFET 111 and a source/drain terminal of PFET 113 are connected to Domain 2 voltage supply rail 107 (at voltage VDD2).